

1900 UNIVERSAL FIRST-ARTICLE PROGRAMMING SYSTEM

PRODUCT SPECIFICATION SHEET
1900 FIRST-ARTICLE PROGRAMMING SYSTEM



POWERED BY 9TH GENERATION PROGRAMMING TECHNOLOGY



The industry's **only** programming solution that offers both the **fastest** programming speeds with **true universal** device support.

With 9THGEN Universal Site Technology, the 1900 Delivers Measurable Cost Savings

High Speed Programming

- High programming speed for MCUs, eMMC, NAND, NOR and Serial Flash
- Up to 100MBytes/s for industry's fastest program/verify times
- Download image files up to 25MB/s to all programmers simultaneously
- Faster programming times reduces the number of systems, sites and sockets you need to buy
- Up to 9 times faster than competing universal programmers
- The Largest Memory Support in the industry - 256GB, upgradable to 512GB
- Upgradable to 2900 for volume production

True Universal Support

- True universal support One solution for all your programming requirements
- With 240 pin drivers, the 1900 supports a wider range of devices on the same socket reducing your total investment
- Ultra universal site and socket technology streamlines your first article and production programming
- Compatible with existing 7th and 8th gen socket cards and algorithms so our customers can retain the value of their investment in assets
- Vast library of currently supported sockets means faster time to market for your next project

Technology Designed to Deliver Value

- As newer and faster devices are introduced onto the market Vector Engine Co-Processor® technology adapts to the faster speeds, delivering more value with improved performance
- BPWin User-friendly interface includes all the software features you need to run your production programming operation. Process control, IP protection, API for custom applications, monitoring, traceability and External Serialization Server all help you deliver a quality product.
- Economical and efficient receptacle-base socket card design reduces your cost for replacement sockets

Complete Ecosystem

- BPM has ownership of all designs, manufacturing and support for all programming sites, robotics, vision systems, and software, so we can provide unmatched support and responsiveness
- Reduce your time to market by doing New Product Introduction/First Article through Automated Production with the same hardware, algorithms and software
- 1900 for Fast First Articles,
 2900 for Manual Production,
 3900 and 4900 for Automated
 Production

1900 UNIVERSAL FIRST-ARTICLE **PROGRAMMING SYSTEM**

Product Specifications PROGRAMMING HARDWARE **PIN DRIVERS** Quantity: 240-pins drivers total, Power: 90W universal ground transisters Architecture: 9THGEN Concurrent Programming System with Vector Engine Co-Processor® 48 fully universal drivers with vcc, vpp, digi-Programming Sites: 1 per site, 1 socket per site tal and clock annual, may be performed on site with included Calibration: 96 high speed digital and clock pins Vpp Slew Rate: 40V/ms to 6V/us Site Diagnostics: RAM, communications, calibration, timing, Vpp Range: 0V to 25V LEDs, fans, pinoe, power supplies, Ipp Range: Up to 1.2A total voltage/current/slew for vpp and vcc, high cur-Vcc Slew Rate: 40V/ms to 4V/us rent vcc mode, digital pin drivers, and relays. Vcc Range: 0V to 13V **Daughter Card Diagnostics:** Ground Transistors, digital driver path to pro-Icc Range: 0-2A grammer, dcard LEDs, customizable diagnostics 0V to 4.5V Digital Range: Digital Rise Time: **Continuity Test:** Each pin, including Vcc, ground, and signal pins, Protection: Vpp, Vcc, and digital pin drivers are promay be tested before every programming optected from ESD events. Vpp and Vcc drivers eration are also protected from overcurrent. Memory: 256GB per site, upgradable to 512GB Clocks: 800kHz to 64MHz Communications: USB 2.0 Data Pattern Broadcast: 25MB/s SOFTWARE Firmware ROM: Software automatically performs firmware download Required: BPWin 20ns cycle Binary, Intel, Motorola, RAM, straight, hex, Peak Verify Bandwidth: File Type: Pass, Fail, Active, Start LEDs, and start button on User Interface: hex-space, Tekhex, Extended Tekhex, ASCII, each site; PC display shows systems status at a hex, OMF, LOF, MER, STAPL, and others glance; auto-start mode automatically begins **Device Processes:** ID check, blank check, continuity, auto start, programming when device is inserted blank, checksum, compare, program, test, Programming Yield: Assured by independent universal pin drivers verify, erase, secure on each socket, short distance from pin drivers File Loading: no download time because programmer is to device, and accuracy of waveforms PC controlled; **MECHANICAL SPECIFICATIONS Devices Supported:** NAND Flash, NOR Flash, Serial Flash EPROM, EEPROM, Managed NAND, MCU Operational Temperature: 55° to 90° F (13° to 32° C) Protection: Overcurrent shutdown, power failure shut-Relative Humidity: 30-80% down, ESD protection, reverse insertion, Dimensions: length 304.800mm x width 304.800mm banana jack for ESD wrist strap working height (excluding sockets) 73.025mm Additional Features: Automatic file type identification, Jobmas-Mass: 3.7kg ter™, BERT™, Auto Range, Data Editor, Revi-**OPTIONS** sion History, Device and Algorithm informa-Support for existing FX and FVE socket modules. Socket Modules: tion, Searchable help menu, BBM, ESS, ses-Universal 1900/2900 socket cards with 144 sion logging, on-line help

universal pins. Available Socket Cards including, but not limited to, standard PLCC, CSP, BGA, µBGA, SOIC, QFN, MLF, LAP, QFP, TSOP, LCC, SDIP, SIMM, Receptacle Socket

Other Options: Advanced Feature Software, simple and com-

plex serialization, CJob, Monitor and CJob Con-

trol (API)

WARRANTY

Hardware: **One Year Hardware Warranty One Year Software Support** Software:



Operating Systems:

Algorithms:

Microsoft Windows XP Professional,

certified algorithms for new devices

Large library of existing algorithms. All al-

gorithms are manufacturer approved or certified (if required) - BPM Microsystems has

an excellent record of being first to provide

Windows 7 32bit

Setting the Standard in Device Programming

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